MULTIPLE PARTS OF A VARIABLE AND ITS COMPLEMENT

MULTIPLE vars

YAMMIC N42ABDS

Lecture 22
The diagram represents a binary adder circuit for modular arithmetic. The equation $2^{16} = 65,536$ rounds up.

A circuit that is a circuit to add 2 numbers.

It is required to design a 8-bit.

How many stages (rows in a TT) would large scale logic design?
Families of LSI ICs

= Read Karte Chap 4 up to 4.2.1

Family of LSI / MSI Circuits

Look-up Tables

FPGA

Modular Design: PLA/PAL
over a Smaller # of Channels or Links

Information Units To Be Transmitted/Received

MUXs/DEMUXs Allow a Larger Number of

3) MUX & DEMUX

- A = B
- A < B
- A > B

2) Commentaries (Covered)

- Building Blocks
- Addresses

1) Arithmetic Units (Covered)
Simplified Notation

\[
\begin{array}{cccc}
& 0 & 1 & 1 \\
Y = 1 & 1 & 0 & 0 \\
Y = 0 & 1 & 0 & 0 \\
Y = I & 0 & 0 & 0 \\
\end{array}
\]

Select I, so Y = I.