Le Circuit 16

Electronics Characteristics

Input

0.1" Logic

Max Current

2.0 mA to 0.0 mA

-400 mA to 0.0 mA
In a more formal sense, the

\[
\begin{pmatrix}
\text{In} & \text{Out}
\end{pmatrix}
\begin{pmatrix}
\text{In} & \text{Out}
\end{pmatrix}
\text{ is the minimum of the two outputs. That is,}
\]

Since the maximum output

levels.

It spells internally the logic.

CAUTION/BE TRUE OUTPUT NULL
BY ALL INPUTS EXCEPTS TRUE
BECAUSE THE CURRENT BECAUSE
BECAUSE THE CURRENT BECAUSE
ON OUTPUT TO 0
\underline{\text{Fan-out: }}
\]

\[
1\text{C circuits, thus there are not aspects. True because. An}
\]

Initially a circuit should have infinite input

impedance, and zero output impedance.
\[
\min \left( \left[ \begin{array}{c}
0.4 \\
0.2
\end{array} \right], \left[ \begin{array}{c}
0.4 \\
0.2
\end{array} \right], \left[ \begin{array}{c}
0.4 \\
0.2
\end{array} \right] \right) = S \leq T
\]

So, the 88/86 was the DC fan-out of:

And: 0.4 W A \in 20 \text{ mA} e \in 1.0 \text{ A} e \in \text{mA characteristics}

On the other hand, one 74LSXX's input characteristics:

Or a DC fan-out of 1 T

\[
\min \left( \left[ \begin{array}{c}
1.6 \\
2.0
\end{array} \right], \left[ \begin{array}{c}
1.6 \\
2.0
\end{array} \right], \left[ \begin{array}{c}
1.6 \\
2.0
\end{array} \right] \right) \leq T
\]

Has a DC fan-out of:

So, 0.4 W A \in 0.0 \text{ A} and 0.0 \text{ A} < 400 \text{ mA} e \in \text{mA characteristics}

And 74LSXX has a Max. Input Current e \in 1.6 mA e \in \text{mA}.
A single output. In that case, Latches/Registers of the

Connect several thousand of (74LSXX) instead. Does NOT mean those we can actually

Lead to a DC fan out of thousands of TTL's.

Based on the above a (CMOS) circuit may


circuit or the 74/86

 Either 174XX or 74LSXX or a distance

Implication that you may connect, at most,

AND the 74LSXX, a fan-in of 5 (74LS1), which

So to make simply a 7400 or 1 (TTL),

In a more informal way, the 88/86 is