The data is indirectly addressed in the registers.

Data is indirectly addressed in the registers.

Data is provided in the registers.

Register

Main Types

\( \phi \) MOV AX, \( \phi \)

\( \phi \) MOV AL, \( \phi \)

MOV AL, Source = OP{CODE-OCT-DEC}

DATA Address Modes

Le Cunze 5
Register Indirect

Assembler:

```assembly
MOV [BX], CL
```

Description:
- Type: Register
- Immediate
- Memory

**Notes:**
- The address in BX is used to access memory.
- The CL register is moved to the memory location pointed to by BX.

### Assumptions
- DS = 1000H
- BX = 1234H
- AX = C345H

### Example
- MOV [BX], AX
35 = 1000

Vext = 1000

Assuming

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A
Note: Only (x², x⁴) or (x⁶)

\[
\begin{align*}
75 & = 10000^m \\
& \text{Assume: } 10000^m \\
& \text{After} \\
& \text{E.g. Before}
\end{align*}
\]

\[
\begin{align*}
1235 & \div 1234 \\
30 & \div 30 \\
68 \times 10001200 \\
51 \div 10014 \\
A \times 0030 \\
A \times 0030 \\
\end{align*}
\]